

A BIPOLAR TRANSISTOR HAVING LOW EXTRINSIC BASE RESISTANCE

Publication number: WO9952138

Publication date: 1999-10-14

Inventor: JEROME RICK C

Applicant: UTMC MICROELECTRONIC SYSTEMS I (US)

Classification:

- International: H01L21/331; H01L29/10; H01L29/732; H01L21/02;
H01L29/02; H01L29/66; (IPC1-7): H01L21/331;
H01L29/732

- European: H01L21/331F; H01L29/10B; H01L29/732B

Application number: WO1999US07644 19990407

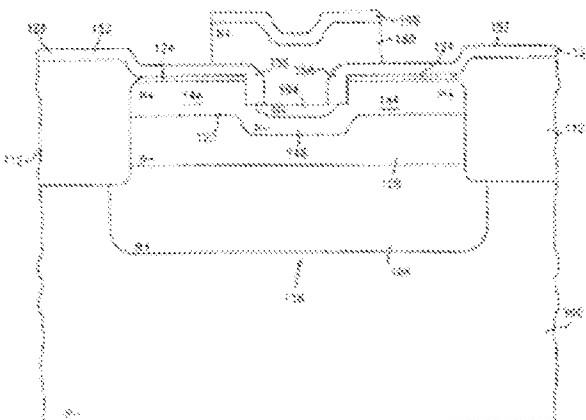
Priority number(s): US199800566536 19980408

Cited documents:

- DE19614162
- JP64002361
- JP3190139
- JP7142702
- JP2079436

[Report a data error here](#)**Abstract of WO9952138**

A method is disclosed for forming a vertical bipolar transistor having a relatively low value of extrinsic base resistance. The method creates the transistor having a recessed emitter and a single polysilicon layer that functions as the emitter contact. The polysilicon emitter contact extends downward into a shallow trench formed in an upper portion of a layer of silicon that is heavily doped to form the extrinsic base regions on each side of the shallow trench. At the bottom of the shallow trench is the single crystal emitter region which overlies the intrinsic base region of the transistor. In turn, the intrinsic base region overlies the collector region.



Data supplied from the esp@cenet database - Worldwide